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Docket No.: P2000,0167 CIP

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By: _____ Date: July 11, 2002

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Herbert Palm et al.
Applic. No.	:	09/927,573
Filed	:	August 9, 2001
Title	:	Memory Cell, Memory Cell Configuration and Fabrication Method
Art Unit	:	2811

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

S i r :

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

English translation of relevant parts of Taiwanese Patent Application TW 84 113 408, dated July 11, 1997, thin-film transistor and its manufacturing method;

Taiwanese Examination Report dated June 24, 2002.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

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For Applicants

Date: July 11, 2002

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